

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination OGAWA ET AL.	
		Examiner Robert E. Fennema	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,697,939 B1	02-2004	Kahle, James Allan	712/244
	B	US-			
	C	US-			
	D	US-			
	E	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Patterson, David. Hennessy, John. "Computer Architecture: A Quantitative Approach". Morgan Kaufmann Publishers Inc, Second Edition, 1996. Pages 635-636.
	V	Rotenberg et al. "Trace Cache: a Low Latency Approach to High Bandwidth Instruction Fetching". 1996.
	W	Wirthlin et al. "The Nano Processor: a Low Resource Reconfigurable Processor". 1994.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.